| | THEVENIN'S THEOREM | Issue No.: 01 Rev No.: 5.2 | Date: 7 th July 2000 Rev. Date: 20 th July 2018 |
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| IMAGINE ASPIRE ACHIEVE | | Clause: Nil | Page: 1 of 3 |

Objective: - To verify Thevenin's theorem for dc circuit.

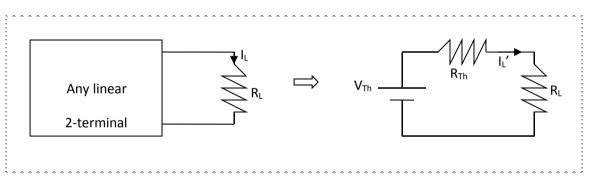
Apparatus Used: - Network kit, Bread board, resistances & connecting wires.

Theory: -

"Any linear two terminal network can be replaced by an equivalent network consisting of a voltage source (V_{Th}) in series with a resistance (R_{Th}) .

Where, V_{Th} = Open circuit voltage at load terminals.

 R_{Th} = Equivalent resistance at load terminal when sources are made inoperative.



Observation Table: -

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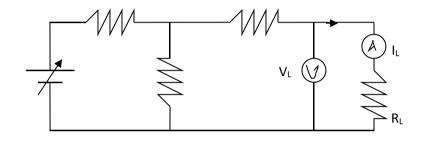
| S.N. | IL (mA) | V _L (V) | $R_{L} = \frac{V_{L}}{I_{L}}$ (\Omega) | V (V) | l (mA) | $R_{\rm Th} = \frac{V}{I}$ (\Omega) | V _{тн} (V) | $I_{L}' = \frac{V_{Th}}{R_{TH} + R_{L}}$ (mA) | $% Error = \frac{I_L - I_L}{I_L} * 100$ |
|------|------------|-----------------------|--|----------|-----------|-------------------------------------|------------------------|---|---|
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| Prepared By :- Husain AhmedReviewed By :- Mohamed SamirApproved By :- Dr. A.K. Gupta | |
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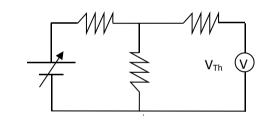
| UNIVERSITY IMAGINE ASPIRE ACHIEVE | THEVENIN'S THEOREM | Issue No.: 01 Rev No.: 5.2 Clause: Nil | Date: 7 th July 2000 Rev. Date: 20 th July 2018 Page: 2 of 3 |
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Circuit Diagram: -

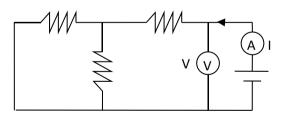
(A) For load current (I_L) and Load Resistance (R_L)



(B) For V_{Th}

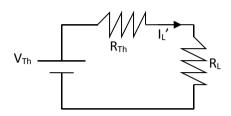


(C) For R_{Th}



| THEVENIN'S THEOREM | Issue No.: 01 Rev No.: 5.2 Clause: Nil | Date: 7 th July 2000 Rev. Date: 20 th July 2018 Page: 3 of 3 |
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(D) For I_L'



Result: - Thevenin's theorem is verified with some permissible error which is allowed in practical.

Precaution: -

- 1. Check all the resistances and connecting wires are properly connected.
- 2. Terminals of voltage source of the kit should not be short circuited only circuit on the board should be short circuited.
- 3. Current in the ammeter is in mille amperes not in amperes while voltage is in volts.
- 4. Check the connecting lead if voltage or current is not displayed on respective meters.
- 5. The current and voltage given to ammeter & voltmeter respectively should not exceed beyond their maximum range.

Answer the following questions

- Q1. What do you understand by open circuit and short circuit?
- Q2. Why the load resistance is open circuited in Thevinin's and short circuited in Norton's?
- Q3. AC is more dangerous or DC and why?